



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#104  
7.3.03

In re Application of: Timothy J. Dupuis et al.

Filed: January 10, 2000

For: INTEGRATED MODEM AND LINE-ISOLATION CIRCUITRY  
WITH HDLC FRAMING AND ASSOCIATED METHOD

Serial No.: 09/480,747

Group Art Unit: 2634

Examiner: Munoz, Guillermo

Atty Docket No.: SILA:054

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Technology Center 2600

Pursuant to 37 C.F.R. 1.8, I certify that this correspondence is being deposited with the U.S. Postal Service in a first class, postage prepaid envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date below:

6/25/03

Diane C. Potts

Date

Name

ATTN: OFFICIAL DRAFTSMAN  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**SUBMISSION OF FORMAL DRAWINGS**

Applicants hereby submit the formal drawings for the above-referenced application and request that these drawings be accepted for filing.

Respectfully submitted,

  
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